

FIG. 1

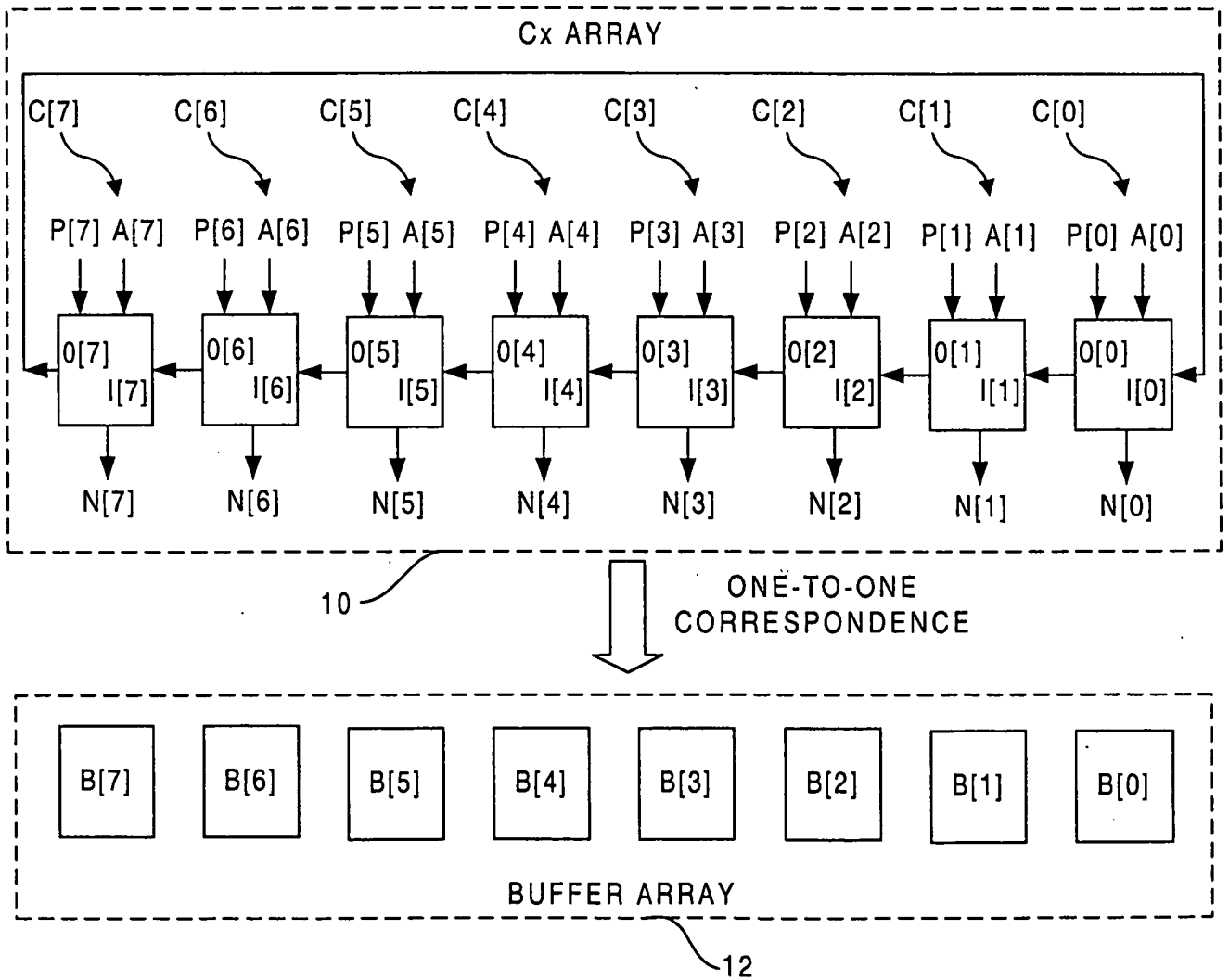
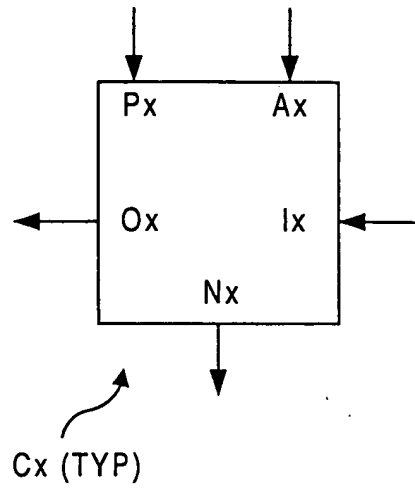


FIG. 2

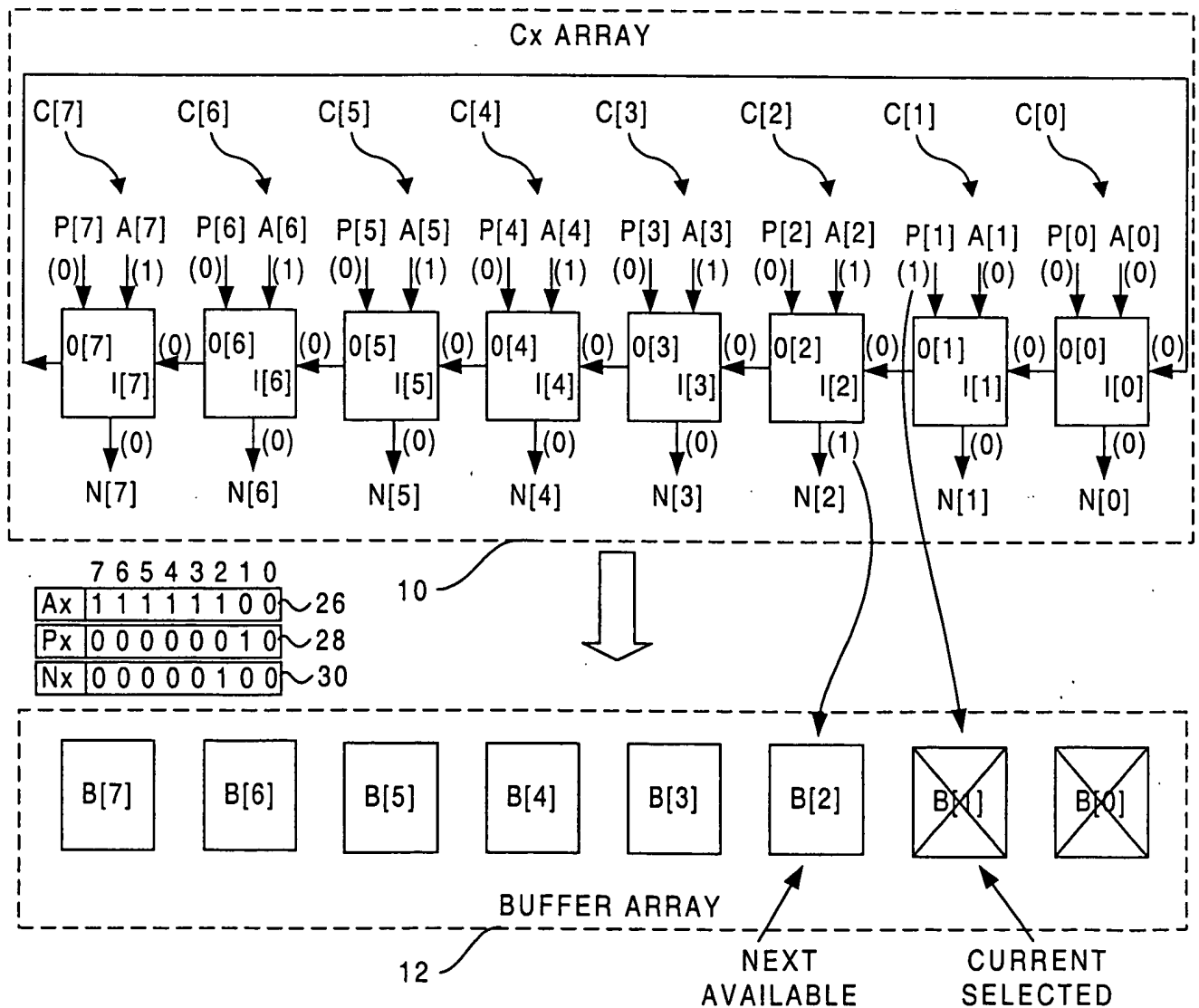
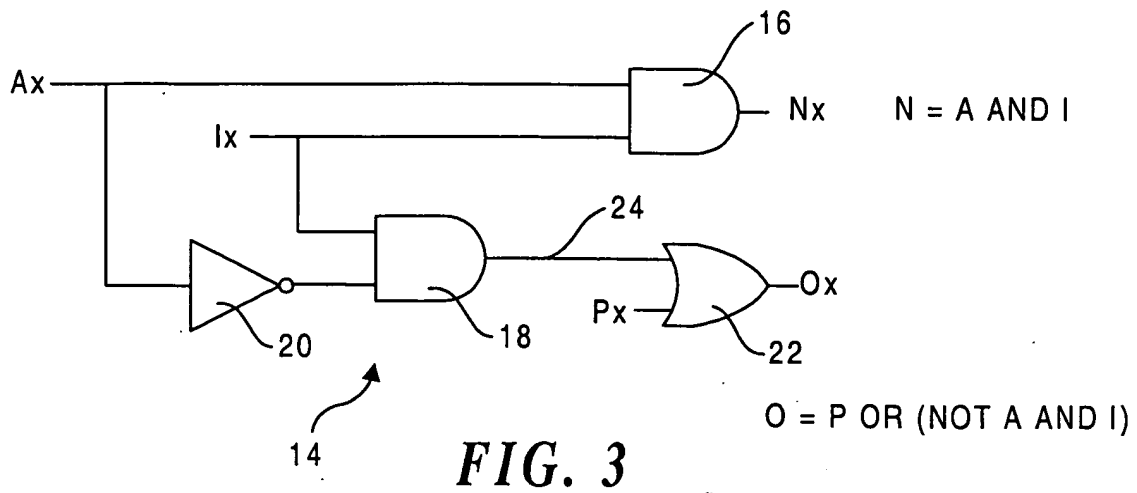


FIG. 4

PROCESSOR

ARRAY OF BUFFERS

36

BUFFER ALLOCATION CIRCUIT
(ARRAY OF COMPUTATIONAL CELLS)

34

32

The diagram shows a rectangular box labeled 'PROCESSOR' at the top left. Inside the processor box, there are two main components. The first is a rectangular box labeled 'ARRAY OF BUFFERS' with the reference numeral '36' below it. The second is a larger rectangular box labeled 'BUFFER ALLOCATION CIRCUIT (ARRAY OF COMPUTATIONAL CELLS)' with the reference numeral '34' below it. A line with the reference numeral '32' at the end originates from the bottom left corner of the processor box and extends downwards and to the right.

FIG. 5